

ABSTRACT OF THE DISCLOSURE

A jitter inducing circuit receives a reference pulse train and induces desired amounts of jitter to the rising and/or falling edges of the pulses. First and second delay blocks 16 and 18 alternately delay the selected edge or
5 edges of the provided reference pulse train interval by preset delay times for every interval. A signal composer 46 composes the outputs of the first and second delay blocks 16 and 18. A delay time setup circuit controls the delay times of the delay blocks 16 and 18. The delay times may change for each interval as a function of time so as to trace a desired function such as a
10 sinusoidal or triangular function.